

|   | Type | L # | Hits | Search Text                                       | DBs   | Time Stamp           |
|---|------|-----|------|---|---|----------------------|
| 1 | BRS  | L1  | 567  | 349/123   | USPA<br>T;<br>US-P<br>GPUB<br>;<br>EPO;<br>JPO;<br>DERW<br>ENT;<br>IBM-<br>TDB- | 2002/10/1<br>6 09:48 |
| 2 | BRS  | L2  | 171  | 1 and pretilt adj<br>angle                        | USPA<br>T;<br>US-P<br>GPUB<br>;<br>EPO;<br>JPO;<br>DERW<br>ENT;<br>IBM-<br>TDB- | 2002/10/1<br>6 10:53 |
| 3 | BRS  | L3  | 81   | 2 and silicon                                     | USPA<br>T;<br>US-P<br>GPUB<br>;<br>EPO;<br>JPO;<br>DERW<br>ENT;<br>IBM-<br>TDB- | 2002/10/1<br>6 11:30 |
| 4 | BRS  | L5  | 11   | 3 and (spac\$3 with<br>(pixel same<br>substrate)) | USPA<br>T;<br>US-P<br>GPUB<br>;<br>EPO;<br>JPO;<br>DERW<br>ENT;<br>IBM-<br>TDB- | 2002/10/1<br>6 09:51 |

|   | Type | L # | Hits | Search Text  | DBs   | Time Stamp           |
|---|------|-----|------|--|---|----------------------|
| 5 | BRS  | L4  | 63   | 3 and (spac\$3 with<br>(pixel or substrate))                   | USPA<br>T;<br>US-P<br>GPUB<br>;<br>EPO;<br>JPO;<br>DERW<br>ENT;<br>IBM-<br>TDB- | 2002/10/1<br>6 10:05 |
| 6 | BRS  | L6  | 69   | 3 and ((spac\$3 or<br>thickness) with<br>(pixel or substrate)) | USPA<br>T;<br>US-P<br>GPUB<br>;<br>EPO;<br>JPO;<br>DERW<br>ENT;<br>IBM-<br>TDB- | 2002/10/1<br>6 10:54 |
| 7 | BRS  | L7  | 11   | 1 and hattori  | USPA<br>T;<br>US-P<br>GPUB<br>;<br>EPO;<br>JPO;<br>DERW<br>ENT;<br>IBM-<br>TDB- | 2002/10/1<br>6 10:53 |
| 8 | BRS  | L9  | 2    | 8 and pretilt adj<br>angle                                     | USPA<br>T;<br>US-P<br>GPUB<br>;<br>EPO;<br>JPO;<br>DERW<br>ENT;<br>IBM-<br>TDB- | 2002/10/1<br>6 10:55 |

|    | Type | L # | Hits | Search Text  | DBs  | Time Stamp           |
|----|------|-----|------|--|--|----------------------|
| 9  | BRS  | L8  | 11   | 7 and ((spac\$3 or thickness) with (pixel or substrate)) | USPA<br>T;<br>US-P<br>GPUB<br>;<br>EPO;<br>JPO;<br>DERW<br>ENT;<br>IBM-<br>TDB | 2002/10/1<br>6 11:09 |
| 10 | BRS  | L10 | 33   | 2 and (alignment with silicon)                           | USPA<br>T;<br>US-P<br>GPUB<br>;<br>EPO;<br>JPO;<br>DERW<br>ENT;<br>IBM-<br>TDB | 2002/10/1<br>6 11:31 |